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PATENTS  
15311-2107

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re The Application of:  
Gilbert M. Wolrich et al.

Serial No.: 09/042,417

Filed: March 13, 1998

For: Reduction of Add-Pipe Logic by Op-  
erand Offset Shift

Examiner: Firmin Backer

Art Unit: 2781 ✓

Cesari and McKenna, LLP  
30 Rowes Wharf  
Boston, MA 02110  
July 19, 2000

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X Request for reconsideration

X Mark-up drawings (5 sheets)  
X Letter to Official Draftsman



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Honorable Commissioner of Patents and Trademarks  
Washington, D.C. 20231

Sir:

**LETTER TO OFFICIAL DRAFTSMAN**

If the Examiner in charge of the above-identified application approves, please substitute the enclosed drawing sheets containing informal versions of Figures 4, 5, 6, 9, and 11 for the corresponding drawing sheets currently in the application.

The accompanying mark-up of Fig. 4 shows that the replacement differs from the originally filed version in that a “FRAC” output has been added to block 34.

The accompanying mark-up of Fig. 5 shows that the replacement differs from the originally filed version in that a dot has been added to indicate that the left input to multiplexor 43's right port is FRAC\_A.


The accompanying mark-up of Fig. 6 shows that the replacement differs from the originally filed version in that the names of blocks 74, 76, and 78's outputs have been changed from SHIFTS\_A, SHIFTS\_B, and SHIFTS\_AB to SHIFT\_A, SHIFT\_B, and SHIFT\_AB.

The accompanying mark-up of Fig. 9 shows that the replacement differs from the originally filed version in that it omits an extraneous "\_AB" notation.

The accompanying mark-up of Fig. 11 shows that the replacement differs from the originally filed version in that the name of the lower input to gates 121 has been changed to DET\_AB.

Please charge any additional fee occasioned by this paper to our Deposit Account No. 03-1237.

Respectfully submitted,



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